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CLAIMS

1. A packaged integrated circuit device comprising:  
a substrate having a recessed central region surrounded by a raised perimeter, the central region and the perimeter being formed together from the same material, the central region having a plurality of contacts for providing electrical connection from conductors external to the substrate to an integrated circuit device; and

an integrated circuit device formed with contacts on a top surface, flipped, and placed against the central region of the substrate such that the contacts of the integrated circuit device meet the contacts of the central region of the substrate.

2. The packaged integrated circuit device of Claim 1 wherein the contacts of the central region of the substrate are electrically connected to solder balls on an external surface of the packaged integrated circuit device.

3. The packaged integrated circuit device of Claim 1 wherein the substrate is formed primarily from a ceramic material.

4. The packaged integrated circuit device of Claim 3 wherein the ceramic material also includes glass, thereby comprising a glass ceramic material.

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5. The packaged integrated circuit device of Claim 1 wherein the contacts of the central region of the substrate comprise solder bumps.

6. The packaged integrated circuit device of Claim 1 further comprising a heat spreader connected to the integrated circuit device with thermal grease.

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7. The packaged integrated circuit device of Claim 6 wherein the heat spreader further contacts the raised perimeter

8. The packaged integrated circuit device of Claim 6 wherein the integrated circuit device and the heat spreader are recessed such that an upper surface of the heat spreader is planar with an upper surface of the raised perimeter.

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9. The packaged integrated circuit device of Claim 1 wherein the raised perimeter incorporates a capacitor electrically connected to power and ground contacts of the integrated circuit device.

10. An integrated circuit package substrate comprising:  
a recessed central region having a plurality of contacts for providing electrical contact to an integrated circuit device; and  
a raised peripheral area incorporating at least one capacitor connected to contacts in the recessed central region.

11. The integrated circuit package substrate of Claim 10 wherein the capacitor has a sandwich construction comprising a plurality of ground plates connected to a ground terminal alternating with a plurality of power plates connected to a power terminal, and insulating material is disposed between adjacent plates.

12. The integrated circuit package substrate of Claim 11 wherein the ground and power terminals are connected respectively to ground and power supply terminals of the integrated circuit device.

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